

**REMARKS**

The indicated allowability of claims 2, 8, and 10-13 is acknowledged with thanks.  
However, it is believed all of the pending claims are allowable over the art.

Claims 2 and 10 have been rewritten in independent form. A new independent claim 14 has been added and a new claim 15 based on original claim 3, but dependent on claim 14 also has been added. Claims 1 and 9 have been canceled.

Claims 3-8 depend directly or indirectly on claim 2, and claims 11-13 dependent directly or indirectly on claim 10 and are allowable for the same reasons as claims 2 and 10, respectively, as well as for their own additional limitations.

New independent claim 14 is not anticipated nor rendered obvious by Okutomi et al. (Japanese Patent Publication Number 2000-174180). Claim 14 requires "a second heatsink plate connected to a rear surface of said semiconductor chip without an insulating substrate of highly conductive material". This feature is not taught by Okutomi et al. A direct connection between the second heatsink plate and the semiconductor chip provides improved heat release. In the semiconductor structure disclosed in the cited reference, in contrast, the second highly-heat-conductive insulating substrate is disposed between the semiconductor devices 51 to 54 and the second heatsink 8. Thus, new claim 14, and claim 15 which depends thereon are allowable over the art.

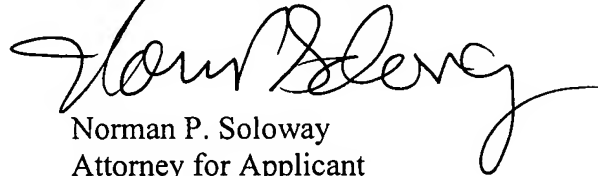
Having dealt with all the objections raised by the Examiner, the Application is believed to be in order for allowance. Early and favorable action are respectfully requested.

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Respectfully submitted,



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